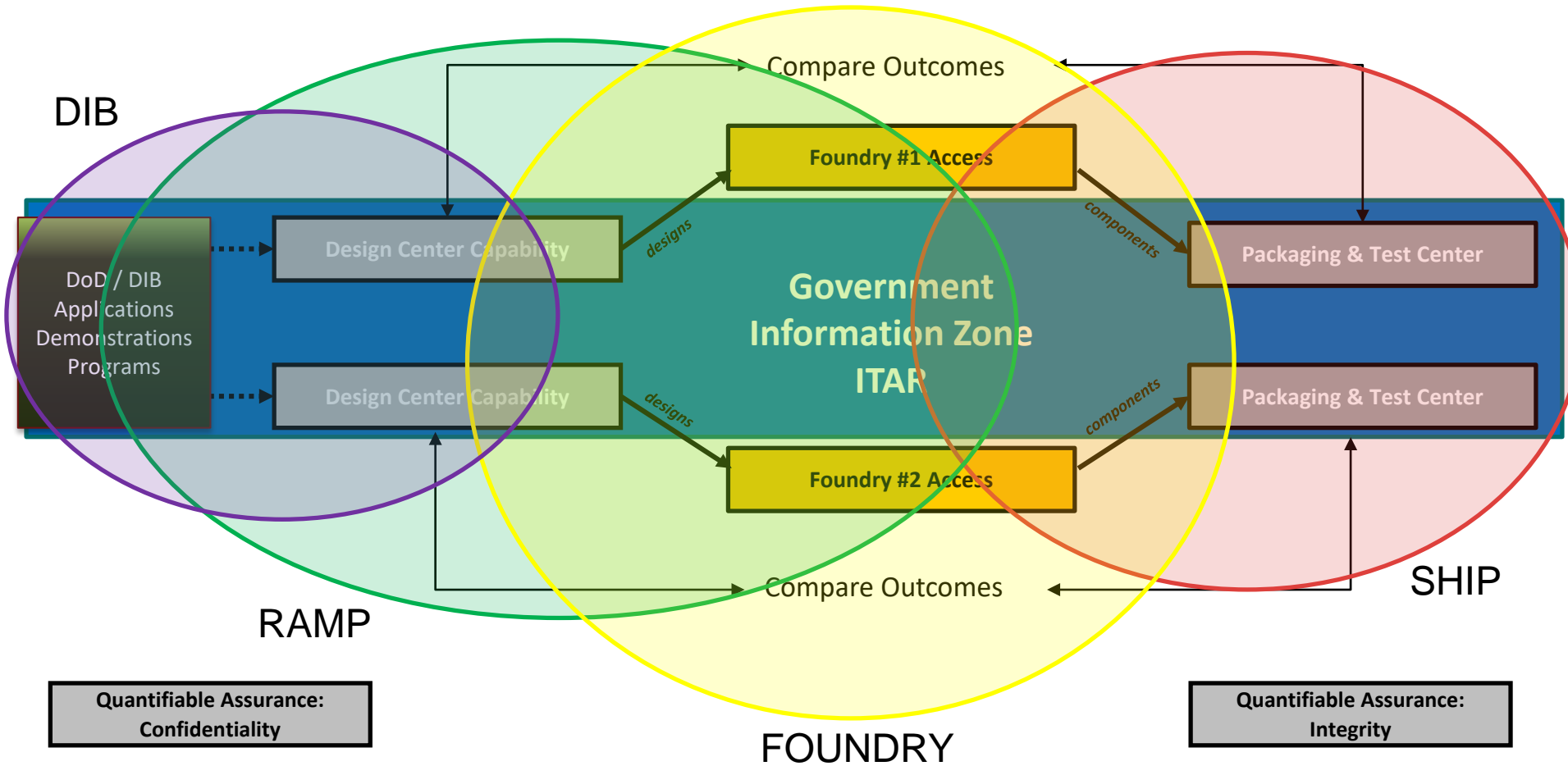




# Rapid Assured Microelectronics Prototypes using Advanced Commercial Capabilities (RAMP) and State-of-the-art (SOTA) Heterogeneous Integrated Packaging (SHIP) Process Flow



**Confidentiality:** the protection of sensitive design information during the implementation and manufacturing process.

**Integrity:** the assurance that no unanticipated or un-attributable alterations of the design were made during the fabrication process.